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PTO/SB/08A (08-03)

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <small>(file as many sheets as necessary)</small>		Application Number	
		Filing Date	December 30, 2004
		First Named Inventor	Yoshinobu TARUTANI, et al.
		Art Unit	
		Examiner Name	
Sheet 1 of 2	Attorney Docket Number	520.44579X00	

U.S. PATENT DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
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FOREIGN PATENT DOCUMENTS						
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
		2000-261307	09/22/2000	JAPAN	2	
		2001-068995	03/16/2001	JAPAN		

Examiner Signature	<i>Jaeyun Lee</i>	Date Considered	1/9/05
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
JH		H. Suzuki et al. "Applications of Synchronized Switching In Series-Parallel-Connected Josephson Junctions", IEEE Transactions on Electron Devices, vol. 37, p.2399, 1990 *cited in the specification on pge 4 and the search report of the PCT procedure	
↓		O.A. Mukhanov et al. "Josephson Output Interfaces for RSFQ Circuits", IEEE Transactions on Applied Superconductivity, vol. 7, p. 2826, 1997 cited in the specification on page 2	

Examiner Signature	Date Considered 11/9/06
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